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09/851,181  
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PTO/SB/97 (08-04)

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- Request for Reconsideration under 37 CFR 1.116 (pp 15)

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AUG. 15 2005  
PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Theodore Vaida, *et al.*

Serial No.: 09/851,181

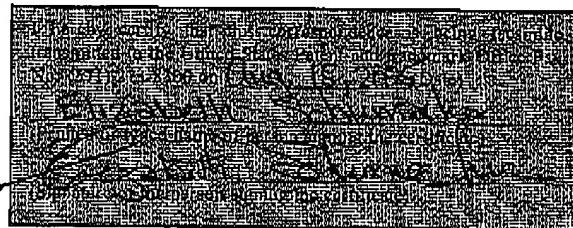
Filed: May 8, 2001

Title: APPLICATION SPECIFIC INTEGRATED CIRCUIT HAVING A  
PROGRAMMABLE LOGIC CORE AND A METHOD OF OPERATION  
THEREOF

Grp./A.U.: 2661

Examiner: Phan, Tri. H.

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Sir:

**REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.116**

In response to the Examiner's Action mailed July 15, 2005, please reconsider the above-  
identified application for the following reasons